



**SYCARD**  
TECHNOLOGY

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***PCChost 1000/1100  
PCI-CardBus Host Adapter  
User's Manual***

*Preliminary*

***M200015-02  
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## ***1.0 Introduction***

The PCChost 1000 host adapter is a PCI plug-in board designed evaluate the Texas Instruments PCI1130 PCI-CardBus bridge device. The PCChost 1100 has the same features of the PCChost 1000, but it uses the PCI1131 PCI-CardBus controller instead.

- Dual sockets support both 16 bit and CardBus cards
- Support for both ISA and PCI interrupts
- LEDs indicate slot power
- Overcurrent LED informs user of error condition
- TPS2202 Vpp and Vcc switch supports all Vpp and Vcc options
- On-board 12V Vpp supply eliminates problems caused by poorly regulated host power
- Adjustable 3.3V Power supply for voltage margin testing
- On-board speaker
- Includes ISA interrupt interface board
- All PCI-1130/1131 signals accessible through test points for easy logic analyzer connection.

### ***1.1 Packing List***

The PCChost 1000/1100 includes the following:

- PCChost 1000 PC Board or PCChost 1100 PC Board
- PCChost 1010 ISA Interrupt Board
- 10 pin cable
- PCI1130 Data Sheet for the PCChost 1000, PCI1131 data sheet for the PCChost 1100
- PCChost 1000/1100 User's Manual
- SystemSoft CardWizard 5 Evaluation Diskettes
- Registration Card

### ***1.2 Specifications***

#### Electrical

Supply Voltage: 5V +-5%

Supply Current: TBD

#### Physical

Width: 3.75"

Length: 8.3"

Thickness: 0.5" Max

Weight: 4 oz

### ***1.3 Related Documentation***

The PCMCIA Developer's Guide - Second Edition

PCI1130 Data Sheet

PCI1131 Data Sheet

TPS2202 Data Sheet

The PC Card Specification - February 1995 Release

## ***2.0 Getting Started***

The PCChost 1000/1100 package contains the PCChost 1000/1100 PCI-CardBus interface, the PCChost 1010 ISA Interrupt Board and a 10 conductor flat ribbon cable.

*Note: A PCChost 1000/1100 installed in a non-PC platform will not require the PCChost 1010 ISA interrupt board and cable.*

### ***2.1 Configuring the Hardware***

The PCChost 1000/1100 has two configurable options, IRQ routing and Vcc select. Before installing the PCChost 1000/1100, the user must first configure these two options. See section 3 for information on configuring these options.

### ***2.2 Installing the Hardware***

The PCChost 1000/1100 can be installed in any available PCI slot. If the user wishes to use ISA interrupts the ISA interrupt board must also be installed and cabled to the PCChost 1000/1100 board.

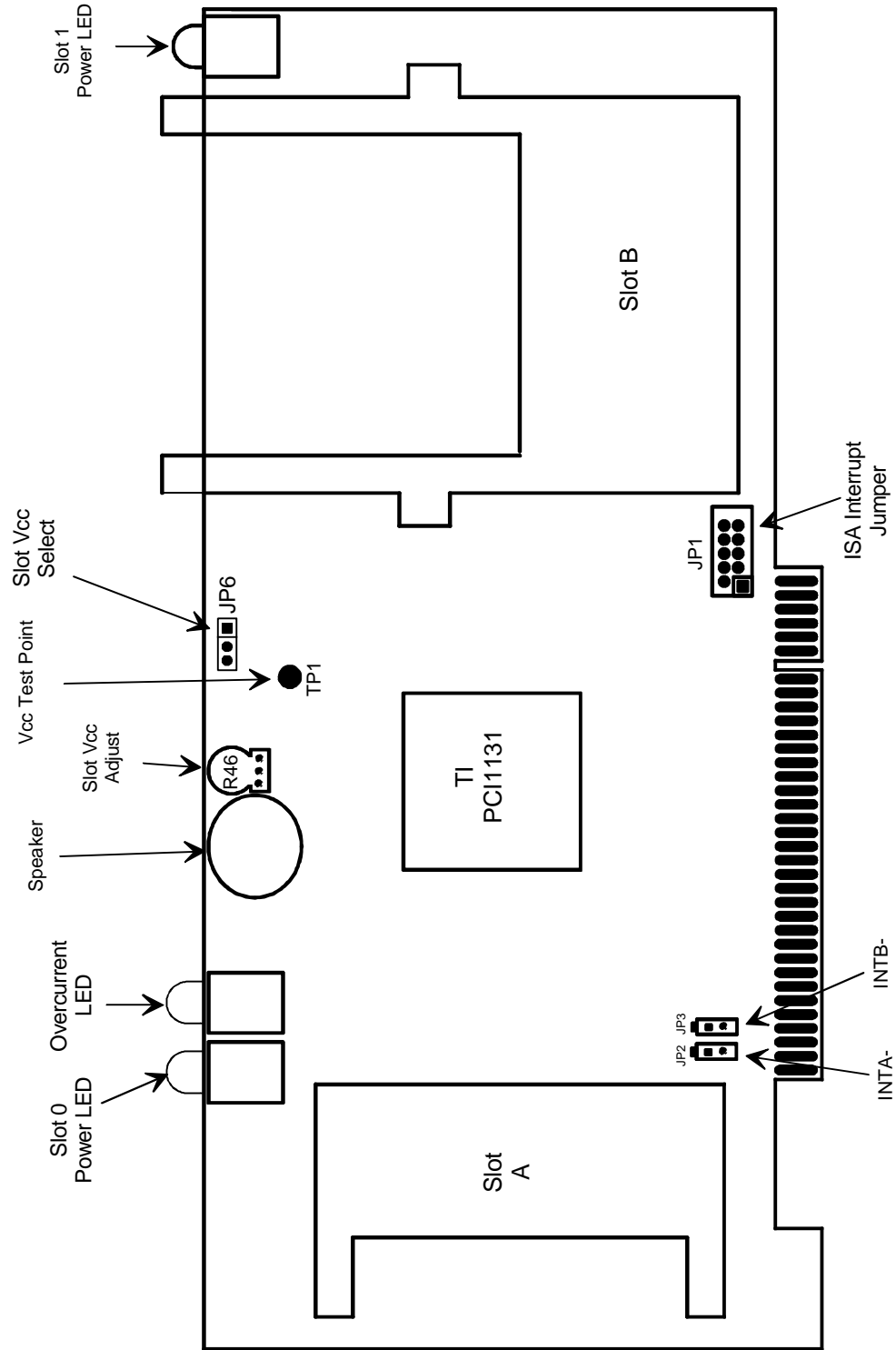


Figure 2.0-1 The PCChost 1000/1100

### 3.0 Using the PCChost 1000/1100

The PCChost 1000/1100 has two user configurable hardware options; IRQ select and slot operating voltage. Each of these options is configured via jumper blocks.

#### 3.1 Configuring Slot Vcc

A single 3.3V on-board power supply provides power for both PC Card slots. Via jumper JP6, the power supply can be configured for fixed or adjustable operation. In the fixed position (JP6 pins 1-2 shorted), the power supply supplies a fixed 3.3V output. When JP6 pins 2-3 are shorted the 3.3V power supply is adjustable from approximately 3.0 to 3.6 volts to allow for voltage margin testing of a PC Card.

JP6 Position	Description
1-2	Fixed 3.3V
2-3	Adjustable 3.3V

**Table 3.1-1 Fixed/Adjustable Vcc Select**

*Caution: Do not change the position of JP6 when power is applied to the PCChost unit. Damage to the PCChost and the host system may occur.*

The trimpot (R46) located next to the speaker adjusts the voltage. To adjust the power supply, connect a voltmeter between the 3.3V test point at TP1 and ground. Remove any installed PC Cards from the slot. Apply power to the board and adjust the trimpot until the desired voltage is obtained.

#### 3.2 Selecting ISA/PCI Interrupts

The PCChost 1000 is capable of routing PC Card interrupts to the PCI bus interrupt requests (INTA and INTB) and/or the ISA IRQ signals. ISA interrupts are connected via an external "paddle" board that is cabled over to the PCChost 1000's JP1 connector. JP2 and JP3 are located on the main PCChost 1000/1100 board. JP10 and JP11 are located on the ISA Interrupt paddle board.

Mode	JP2	JP3	JP10	JP11
PCI Only Host - No ISA Interrupts	On	On	N.A.	N.A.
ISA Only Interrupts	Off	Off	On	On
PCI and ISA Interrupts	On	On	Off	Off

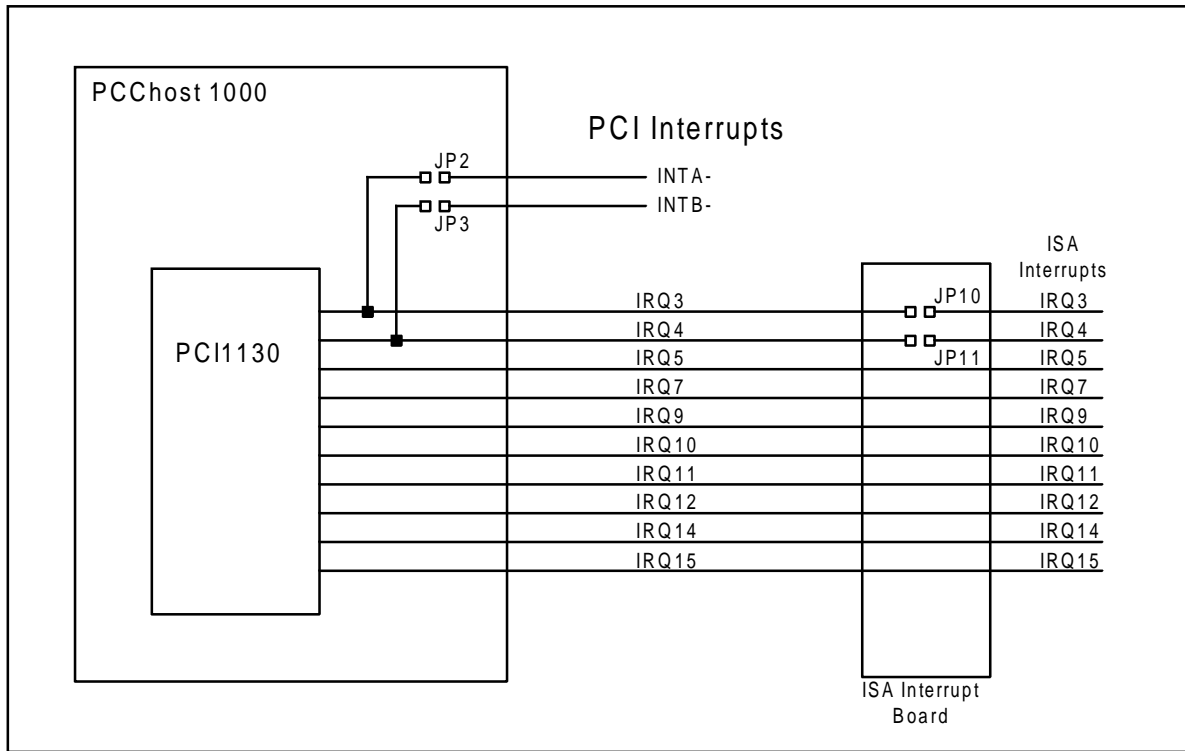


Figure 3.2-1 PCChost 1000 Interrupt Routing

## 4.0 Hardware Description

### 4.1 PCI-1130/1131 PCI-CardBus Bridge

The heart of the PCChost 1000/1100 is Texas Instruments PCI-1130/1131 PCI to CardBus bridge. The PCI-1130 and PCI-1131 chips direct connect to the PCI bus and PC Card slots provides a true-single chip CardBus host implementation. A full detailed description of the PCI-1130/1131 can be found in the accompanying PCI-1130 or PCI-1131 Data Sheet. The PCChost 1000/1100 provides 0.1" spaced headers for accessing all PCI-1130 or PCI-1131 signal pins. These pins can be probed by an oscilloscope or connected directly to a logic analyzer for system debug. A pin map for this header is shown in appendix B.

### 4.2 Power Supply and Power Distribution

The PCChost 1000/1100 contains eight separate power planes. They are as follows:

- 5 Volt PCI power supply
- 3.3 Volt PCI-1130/1131 "Core" power supply
- Adjustable 3.3 Volt card power supply
- 12 Volt programming voltage power supply (Vpp)
- Slot A Vcc supply
- Slot B Vcc supply
- Slot A Vpp supply
- Slot B Vpp supply

The following diagram illustrates the connection of the various power planes.

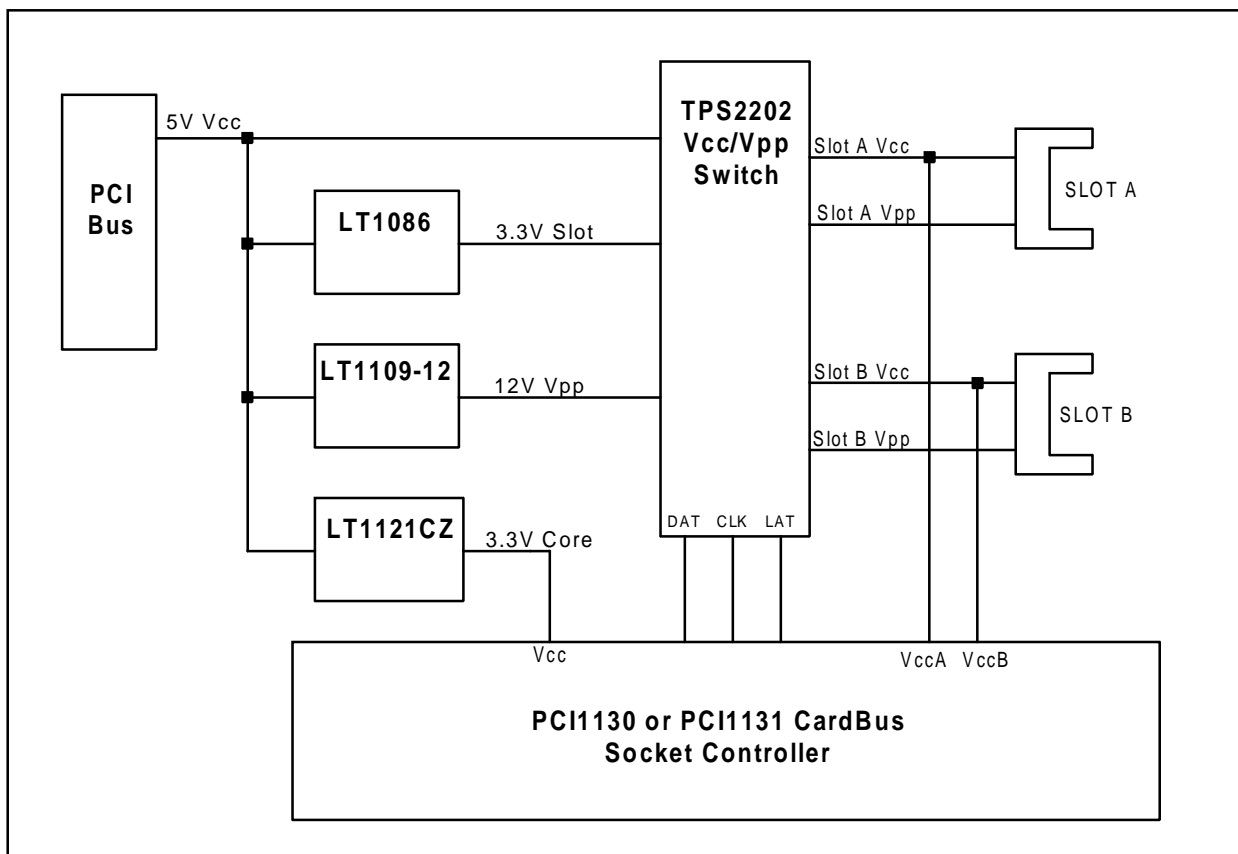


Figure 4.2-1 PCChost 1000/1131 power distribution



### **4.2.1 3.3 Volt PC Card Slot Power Supply**

The 3.3V Card Slot power supply is responsible for supplying power to both PC Card slots. The 3.3V power supply is based on a Linear Technology LT1086 low dropout adjustable regulator. Input to the LT1086 is the PCI 5V supply. The output voltage is determined by a resistor divider across the output pins of the device. The PCChost 1000 includes a jumper (JP6) to select a fixed 3.3V output or an adjustable 3.3V supply capable of supplying 3.0V to 3.6V. 3.3V PC Card slot power is connected to the TPS2202 Vcc/Vpp switch.

### **4.2.2 3.3 Volt PCI1130/1131 "Core" Power Supply**

The 3.3V PCI1130 "Core" power supply provides power for the core portion of the PCI-1130/1131. The core power supply is separately regulated by a LT1121CZ-3.3 fixed 3.3V regulator. The input to the LT1121CZ is the PCI 5V supply. The output connects directly to the PCI1130/1131 Core Vcc pins.

### **4.2.3 12 Volt Power Supply**

A Linear Technology LT1109-12 Vpp generator chip and associated circuitry provide 12 volt power for the Vpp1 and Vpp2 pins on each PC card slot. The input to the LT1109-12 is from the host system 5.0V Vcc. The output connects directly to the TPS2202 Vcc/Vpp switch.

### **4.2.4 Vcc/Vpp switch**

Slot Vcc and Vpp are controlled by the PCI1130/1131 via a external power switch chip. The PCChost 1000 takes advantage of a single chip power switching chip specifically designed for the PC Card environment, the Texas Instruments PDV2202TPS. The PDV2202TPS supports Vcc and Vpp switching for two PC Card slots and provides over-current protection. The control interface to the PDV2202TPS is via a three wire serial connection to the PCI1130/1131.

### **4.2.5 Power Indicators**

Three power indicators display the current slot power status and any overcurrent situation on either slot. Green LEDs adjacent to each slot indicate the present slot power status. A single red LED indicates an overcurrent situation on either socket.

## **4.4 Speaker Driver**

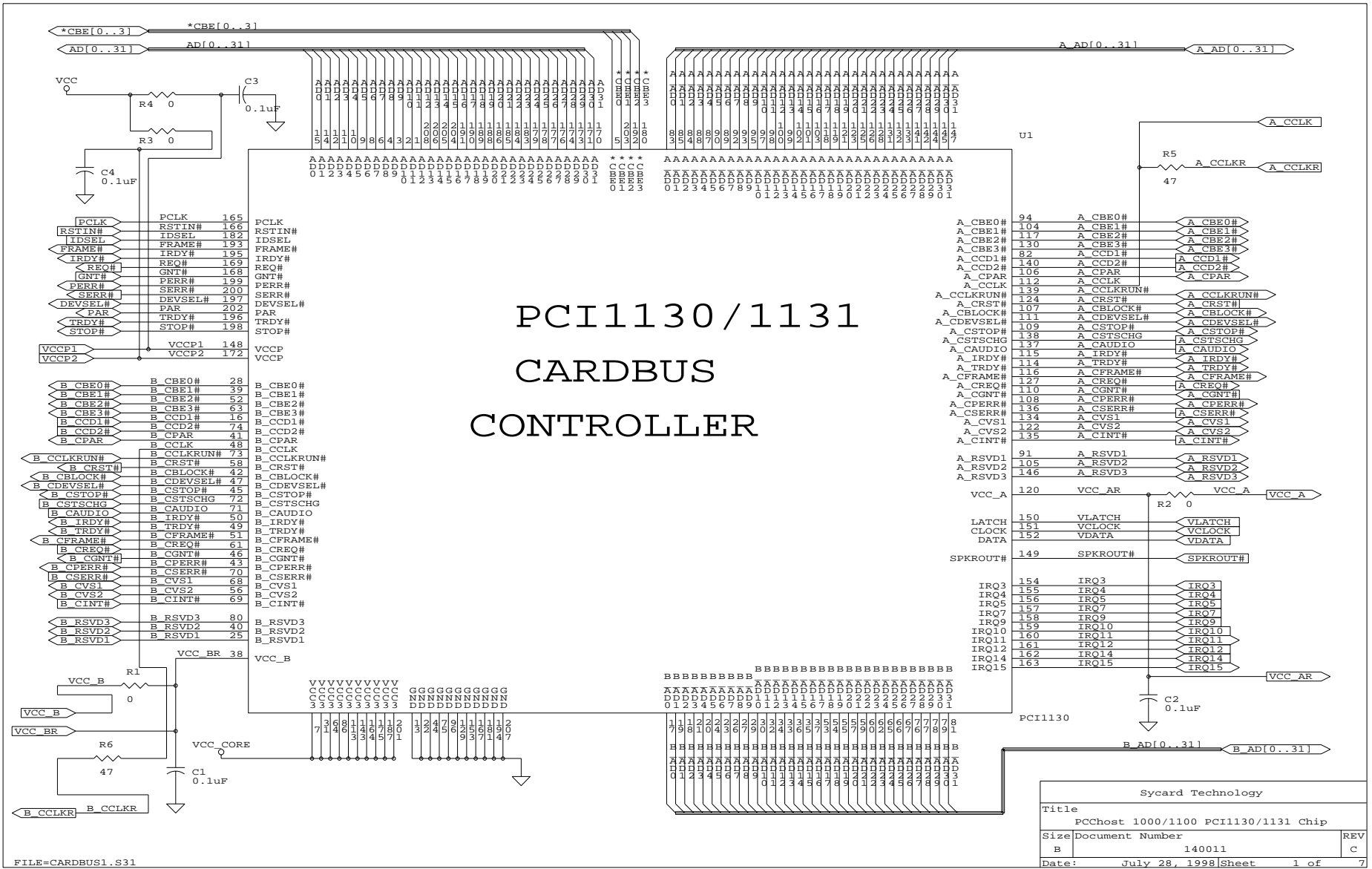
An on-board speaker and driver is connected to the SPKROUT pin of the PCI-1130/1131. The speaker driver is designed to support the audio digital waveform output from a PC Card.

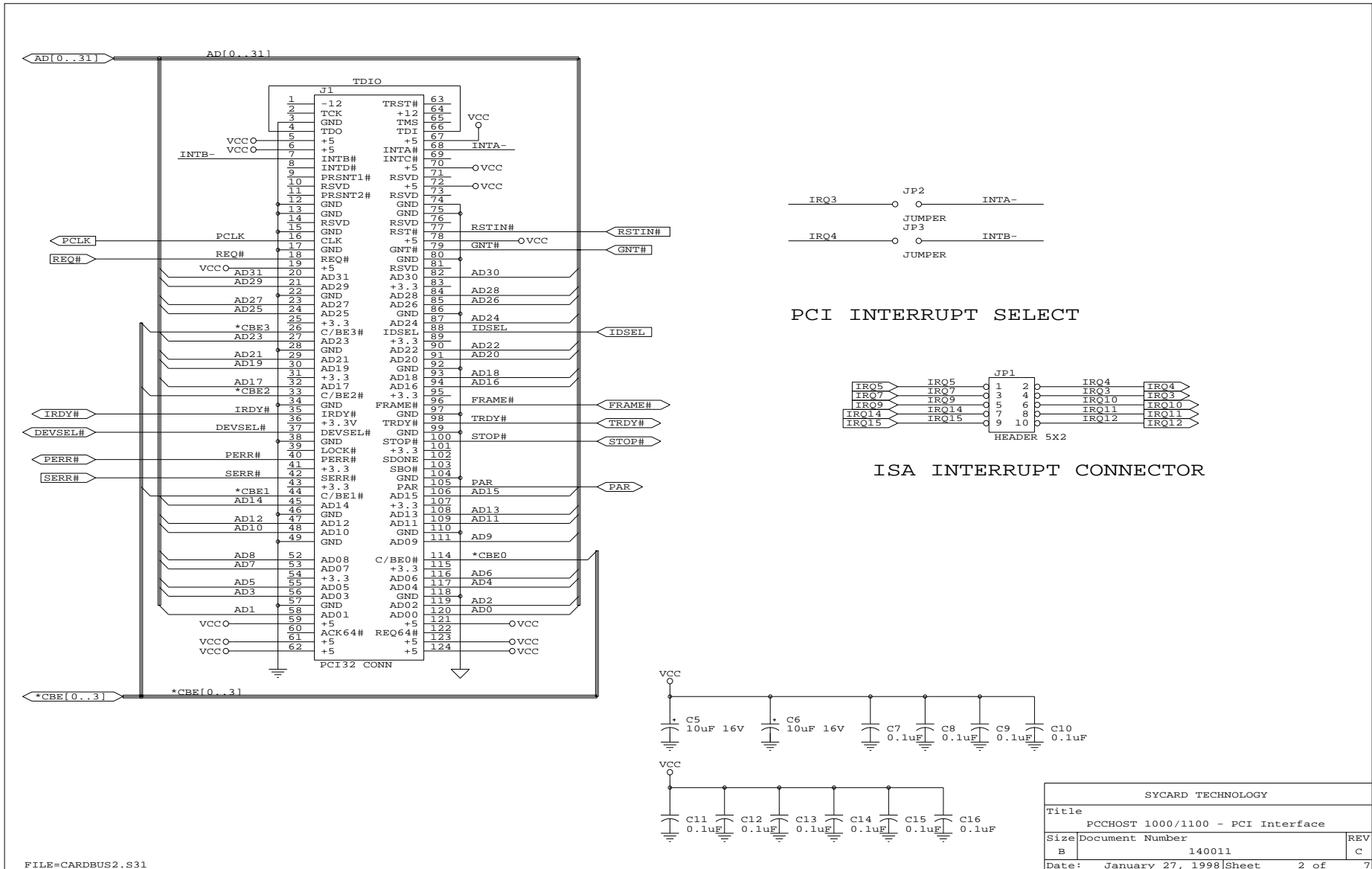
## *Appendix A*

### *PCChost 1000/1100 Schematic*

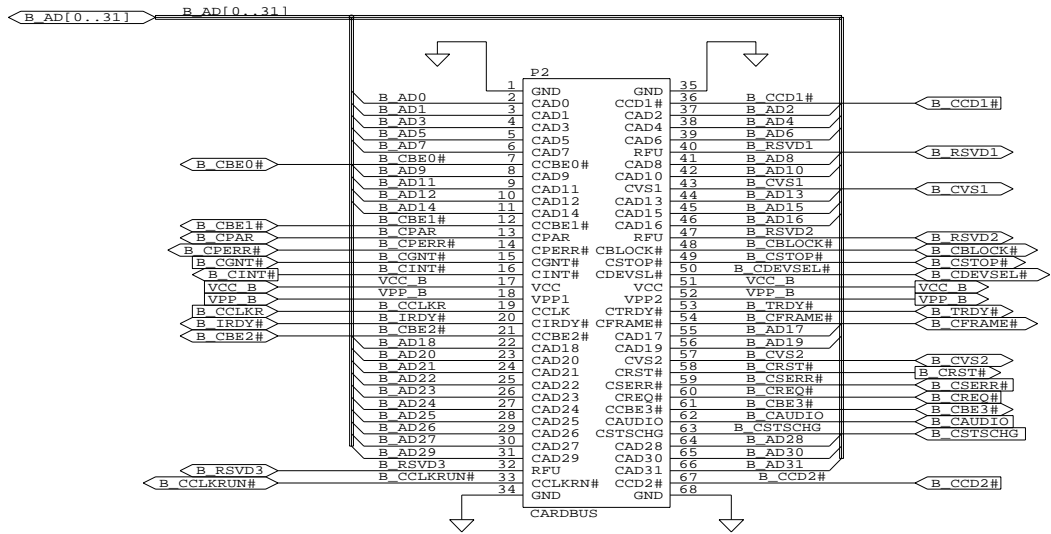


# PCI1130/1131 CARDBUS CONTROLLER

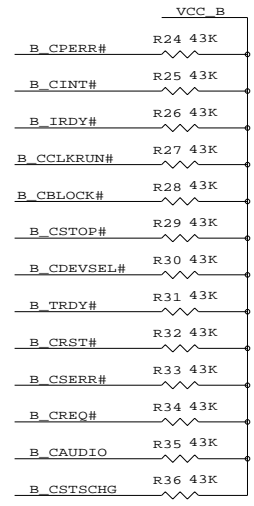




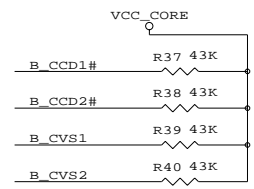


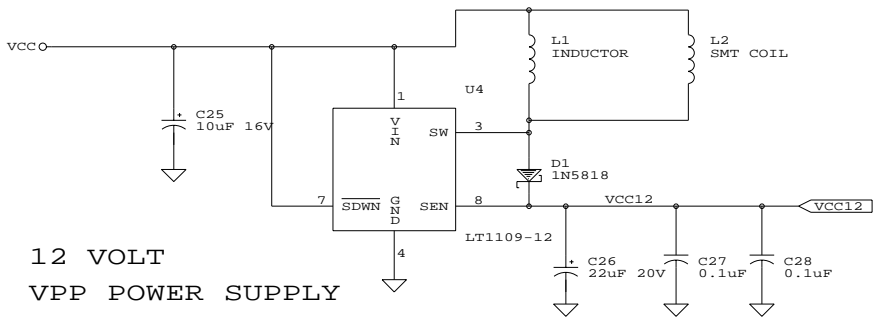


Pullups to Card VCC

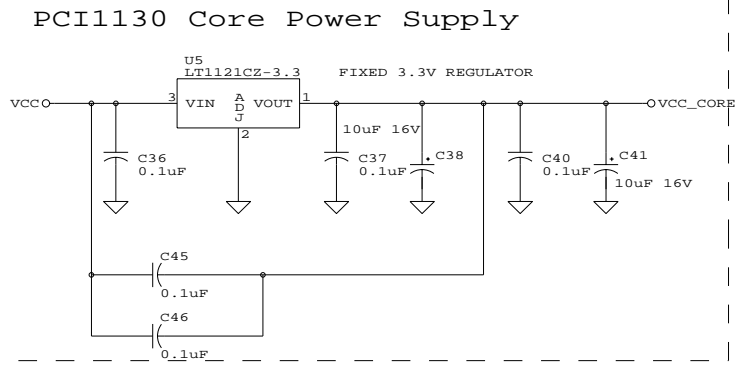


Pullups to System 3.3V



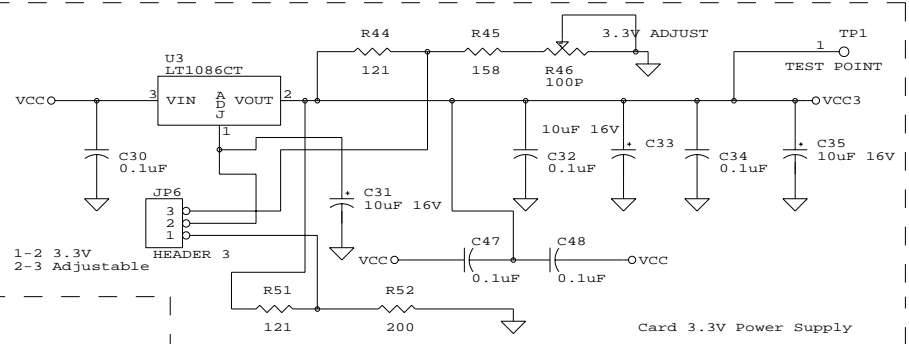
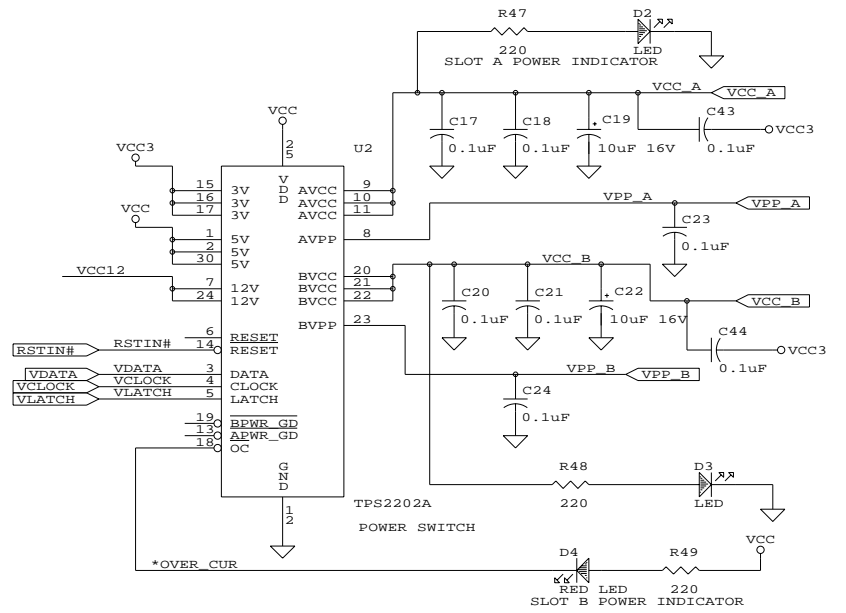
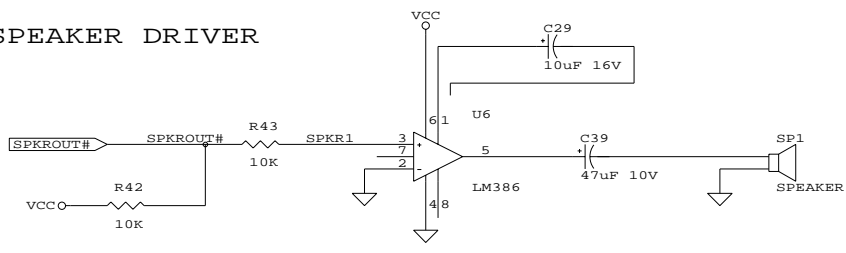


12 VOLT  
VPP POWER SUPPLY



PCI1130 Core Power Supply

SPEAKER DRIVER



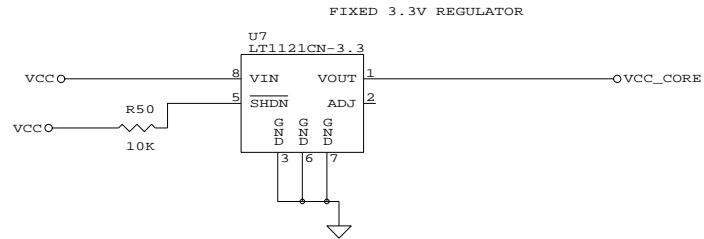
Card 3.3V Power Supply

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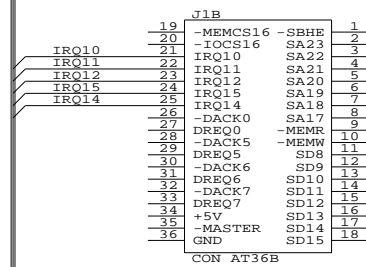
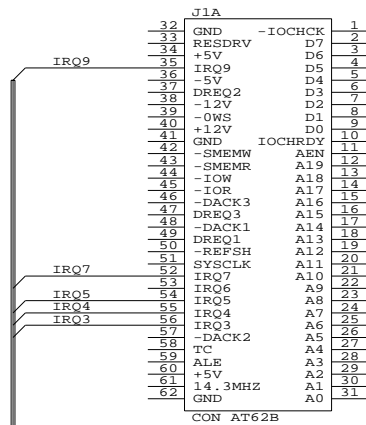
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Size	Document Number	REV
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Date:	January 27, 1998	Sheet 5 of 7



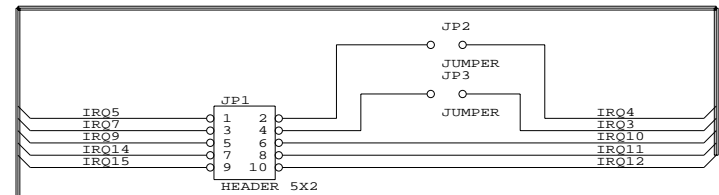
PCI1130 Core Power Supply II



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Title PCChost 1000/1100 - Power Control II		
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Date:	January 27, 1998	Sheet 7 of 7



IRQ[3..15]



# Appendix B

## PCChost 1000/1100 Header Pinout

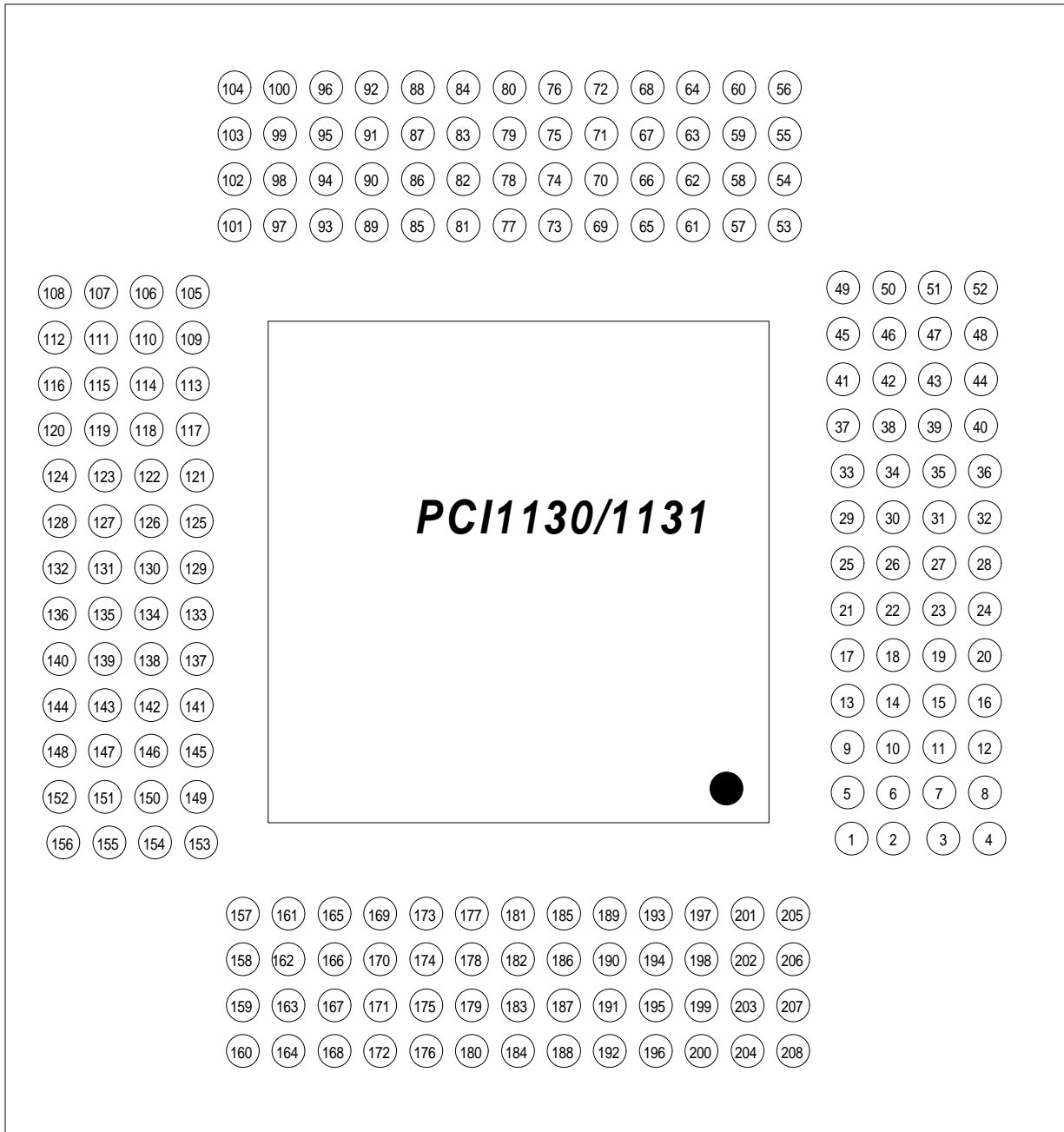


Figure B-1 PCChost 1000/1100 Header

No.	Signal Name	No.	Signal Name	No.	Signal Name	No.	Signal Name
1	AD11	53	B_CAD17	105	A_RSVD	157	IRQ7/PCDMAREQ#
2	AD10	54	B_CAD18	106	A_CPAR	158	IRQ9/IRQSER
3	AD9	55	B_CAD19	107	A_CBLOCK#	159	IRQ10
4	AD8	56	B_CVS2	108	A_CPERR#	160	IRQ11/PCDMAGNT#
5	C/BE0#	57	B_CAD20	109	A_CSTOP#	161	IRQ12
6	AD7	58	B_CRST#	110	A_CGNT#	162	IRQ14
7	V <sub>CC</sub>	59	B_CAD21	111	A_CDEVSEL#	163	IRQ15/RI_OUT
8	AD6	60	B_CAD22	112	A_CCLK	164	V <sub>CC</sub>
9	AD5	61	B_CREQ#	113	V <sub>CC</sub>	165	PCLK
10	AD4	62	B_CAD23	114	A_CTRDY#	166	RSTIN#
11	AD3	63	B_CC/BE3#	115	A_CIRDY#	167	GND
12	AD2	64	V <sub>CC</sub>	116	A_CFRAME#	168	GNT#
13	GND	65	B_CAD24	117	A_CC/BE2#	169	REQ#
14	AD1	66	B_CAD25	118	A_CAD17	170	AD31
15	AD0	67	B_CAD26	119	A_CAD18	171	AD30
16	B_CCD1	68	B_CVS1	120	V <sub>CCA</sub>	172	V <sub>CCP</sub>
17	B_CAD0	69	B_CINT#	121	A_CAD19	173	AD29
18	B_CAD2	70	B_CSERR#	122	A_CVS2	174	AD28
19	B_CAD1	71	B_CAUDIO	123	A_CAD20	175	V <sub>CC</sub>
20	B_CAD4	72	B_CSTSCHG	124	A_CRST#	176	AD27
21	B_CAD3	73	B_CCLKRUN#	125	A_CAD21	177	AD26
22	GND	74	B_CCD2#	126	A_CAD22	178	AD25
23	B_CAD6	75	GND	127	A_CREQ#	179	AD24
24	B_CAD5	76	B_CAD27	128	A_CAD23	180	C/BE3#
25	B_RSVD	77	B_CAD28	129	GND	181	GND
26	B_CAD7	78	B_CAD29	130	A_CC/BE3#	182	IDSEL
27	B_CAD8	79	B_CAD30	131	A_CAD24	183	AD23
28	B_CC/BE0#	80	B_RSVD	132	A_CAD25	184	AD22
29	B_CAD9	81	B_CAD31	133	A_CAD26	185	AD21
30	B_CAD10	82	A_CCD1#	134	A_CVS1	186	AD20
31	V <sub>CC</sub>	83	A_CAD0	135	A_CINT#	187	V <sub>CC</sub>
32	B_CAD11	84	A_CAD2	136	A_CSERR#	188	AD19
33	B_CAD13	85	A_CAD1	137	A_CAUDIO	189	AD18
34	B_CAD12	86	V <sub>CC</sub>	138	A_CSTSCHG	190	AD17
35	B_CAD15	87	A_CAD4	139	A_CCLKRUN#	191	AD16
36	B_CAD14	88	A_CAD3	140	A_CCD2#	192	C/BE2#
37	B_CAD16	89	A_CAD6	141	A_CAD27	193	FRAME#
38	V <sub>CCB</sub>	90	A_CAD5	142	A_CAD28	194	GND
39	B_CC/BE1#	91	A_RSVD	143	V <sub>CC</sub>	195	IRDY#
40	B_RSVD	92	A_CAD7	144	A_CAD29	196	TRDY#
41	B_CPAR	93	A_CAD8	145	A_CAD30	197	DEVSEL#
42	B_CBLOCK#	94	A_CC/BE0#	146	A_RSVD	198	STOP#
43	B_CPERR#	95	A_CAD9	147	A_CAD31	199	PERR#
44	GND	96	GND	148	V <sub>CCP</sub>	200	SERR#
45	B_CSTOP#	97	A_CAD10	149	SPKROUT#/SUSPEND#	201	V <sub>CC</sub>
46	B_CGNT#	98	A_CAD11	150	LATCH	202	PAR
47	B_CDEVSEL#	99	A_CAD13	151	CLOCK	203	C/BE1#
48	B_CCLK	100	A_CAD12	152	DATA	204	AD15
49	B_CTRDY#	101	A_CAD15	153	GND	205	AD14
50	B_CIRDY#	102	A_CAD14	154	IRQ3/INTA#	206	AD13
51	B_CFRAME	103	A_CAD16	155	IRQ4/INTB#	207	GND
52	B_CC/BE2#	104	A_CC/BE1#	156	IRQ5	208	AD12

Table B-1 CardBus PC Card Signal Names Sorted by Terminal Number

Signal Name	No.	Signal Name	No.	Signal Name	No.	Signal Name	No.
A_CAD0	83	A_CSTOP#	109	B_CAD12	34	CLOCK	151
A_CAD1	85	A_CSTSCHG#	138	B_CAD13	33	DATA	152
A_CAD2	84	A_CTRDY#	114	B_CAD14	36	DEVSEL#	197
A_CAD3	88	A_CVS1	134	B_CAD15	35	FRAME#	193
A_CAD4	87	A_CVS2	122	B_CAD16	37	GND	13
A_CAD5	90	A_RSVD	91	B_CAD17	53	GND	22
A_CAD6	89	A_RSVD	105	B_CAD18	54	GND	44
A_CAD7	92	A_RSVD	146	B_CAD19	55	GND	75
A_CAD8	93	AD0	15	B_CAD20	57	GND	96
A_CAD9	95	AD1	14	B_CAD21	59	GND	129
A_CAD10	97	AD2	12	B_CAD22	60	GND	153
A_CAD11	98	AD3	11	B_CAD23	62	GND	167
A_CAD12	100	AD4	10	B_CAD24	65	GND	181
A_CAD13	99	AD5	9	B_CAD25	66	GND	194
A_CAD14	102	AD6	8	B_CAD26	67	GND	207
A_CAD15	101	AD7	6	B_CAD27	76	GNT#	168
A_CAD16	103	AD8	4	B_CAD28	77	IDSEL	182
A_CAD17	118	AD9	3	B_CAD29	78	IRDY#	195
A_CAD18	119	AD10	2	B_CAD30	79	IRQ3/INTA#	154
A_CAD19	121	AD11	1	B_CAD31	81	IRQ4/INTB#	155
A_CAD20	123	AD12	208	B_CAUDIO	71	IRQ5	156
A_CAD21	125	AD13	206	B_CBLOCK#	42	IRQ7/PCDMAREQ#	157
A_CAD22	126	AD14	205	B_CC/BE0#	28	IRQ9/IRQSER	158
A_CAD23	128	AD15	204	B_CC/BE1#	39	IRQ10	159
A_CAD24	131	AD16	191	B_CC/BE2#	52	IRQ11/PCDMAGNT#	160
A_CAD25	132	AD17	190	B_CC/BE3#	63	IRQ12	161
A_CAD26	133	AD18	189	B_CCD1#	16	IRQ14	162
A_CAD27	141	AD19	188	B_CCD2#	74	IRQ15/RI_OUT	163
A_CAD28	142	AD20	186	B_CCLK	48	LATCH	150
A_CAD29	144	AD21	185	B_CCLKRUN#	73	PAR	202
A_CAD30	145	AD22	184	B_CDEVSEL#	47	PCLK	165
A_CAD31	147	AD23	183	B_CFRAME#	51	PERR#	199
A_CAUDIO	137	AD24	179	B_CGNT#	46	REQ#	169
A_CBLOCK#	107	AD25	178	B_CINT#	69	RSTIN#	166
A_CC/BE0#	94	AD26	177	B_CIRDY#	50	SERR#	200
A_CC/BE1#	104	AD27	176	B_CPAR	41	SPKROUT#/SUSPEND#	149
A_CC/BE2#	117	AD28	174	B_CPERR#	43	STOP#	198
A_CC/BE3#	130	AD29	173	B_CREQ#	61	TRDY#	196
A_CCD1#	82	AD30	171	B_CRST#	58	V <sub>CC</sub>	7
A_CCD2#	140	AD31	170	B_CSERR#	70	V <sub>CC</sub>	31
A_CCLK	112	B_CAD0	17	B_CSTOP#	45	V <sub>CC</sub>	64
A_CCLKRUN#	139	B_CAD1	19	B_CSTSCHG#	72	V <sub>CC</sub>	86
A_CDEVSEL#	111	B_CAD2	18	B_CTRDY#	49	V <sub>CC</sub>	113
A_CFRAME#	116	B_CAD3	21	B_CVS1	68	V <sub>CC</sub>	143
A_CGNT#	110	B_CAD4	20	B_CVS2	56	V <sub>CC</sub>	164
A_CINT#	135	B_CAD5	24	B_RSVD	25	V <sub>CC</sub>	175
A_CIRDY#	115	B_CAD6	23	B_RSVD	40	V <sub>CC</sub>	187
A_CPAR	106	B_CAD7	26	B_RSVD	80	V <sub>CC</sub>	201
A_CPERR#	108	B_CAD8	27	C/BE0#	5	V <sub>CCA</sub>	120
A_CREQ#	127	B_CAD9	29	C/BE1#	203	V <sub>CCB</sub>	38
A_CRST#	124	B_CAD10	30	C/BE2#	192	V <sub>CCP</sub>	148
A_CSERR#	136	B_CAD11	32	C/BE3#	180	V <sub>CCP</sub>	172

Table B-2 CardBus PC Card Signal Names Sorted Alphabetically

No.	Signal Name	No.	Signal Name	No.	Signal Name	No.	Signal Name
1	AD11	53	B_A24	105	A_A18	157	IRQ7/PCDMAREQ#
2	AD10	54	B_A7	106	A_A13	158	IRQ9/IRQSER
3	AD9	55	B_A25	107	A_A19	159	IRQ10
4	AD8	56	B_VS2#	108	A_A14	160	IRQ11/PCDMAGNT#
5	C/BE0#	57	B_A6	109	A_A20	161	IRQ12
6	AD7	58	B_RESET	110	A_WE#	162	1RQ14
7	V <sub>CC</sub>	59	B_A5	111	A_A21	163	IRQ15/RI_OUT
8	AD6	60	B_A4	112	A_A16	164	V <sub>CC</sub>
9	AD5	61	B_INPACK	113	V <sub>CC</sub>	165	PCLK
10	AD4	62	B_A3	114	A_A22	166	RSTIN#
11	AD3	63	B_REG#	115	A_A15	167	GND
12	AD2	64	V <sub>CC</sub>	116	A_A23	168	GNT#
13	GND	65	B_A2	117	A_A12	169	REQ#
14	AD1	66	B_A1	118	A_A24	170	AD31
15	AD0	67	B_A0	119	A_A7	171	AD30
16	B_CD1#	68	B_VS1#	120	V <sub>CCA</sub>	172	V <sub>CCP</sub>
17	B_D3	69	B_READY(IREQ#)	121	A_A25	173	AD29
18	B_D11	70	B_WAIT#	122	A_VS2#	174	AD28
19	B_D4	71	B_BVD2(SPKR#)	123	A_A6	175	V <sub>CC</sub>
20	B_D12	72	B_BVD1(STSCHG/RI#)	124	A_RESET	176	AD27
21	B_D5	73	B_WP(IOIS16#)	125	A_A5	177	AD26
22	GND	74	B_CD2#	126	A_A4	178	AD25
23	B_D13	75	GND	127	A_INPACK	179	AD24
24	B_D6	76	B_D0	128	A_A3	180	C/BE3#
25	B_D14	77	B_D8	129	GND	181	GND
26	B_D7	78	B_D1	130	A_REG#	182	IDSEL
27	B_D15	79	B_D9	131	A_A2	183	AD23
28	B_CE1#	80	B_D2	132	A_A1	184	AD22
29	B_A10	81	B_D10	133	A_A0	185	AD21
30	B_CE2#	82	A_CD1#	134	A_VS1#	186	AD20
31	V <sub>CC</sub>	83	A_D3	135	A_READY(IREQ#)	187	V <sub>CC</sub>
32	B_OE#	84	A_D11	136	A_WAIT#	188	AD19
33	B_IORD#	85	A_D4	137	A_BVD2(SPKR#)	189	AD18
34	B_A11	86	V <sub>CC</sub>	138	A_BVD1(STSCHG/RI#)	190	AD17
35	B_IOWR#	87	A_D12	139	A_WP(IOIS16#)	191	AD16
36	B_A9	88	A_D5	140	A_CD2#	192	C/BE2#
37	B_A17	89	A_D13	141	A_D0	193	FRAME#
38	V <sub>CCB</sub>	90	A_D6	142	A_D8	194	GND
39	B_A8	91	A_D14	143	V <sub>CC</sub>	195	IRDY#
40	B_A18	92	A_D7	144	A_D1	196	TRDY#
41	B_A13	93	A_D15	145	A_D9	197	DEVSEL#
42	B_A19	94	A_CE1#	146	A_D2	198	STOP#
43	B_A14	95	A_A10	147	A_D10	199	PERR#
44	GND	96	GND	148	V <sub>CCP</sub>	200	SERR#
45	B_A20	97	A_CE2#	149	SPKROUT#/SUSPEND#	201	V <sub>CC</sub>
46	B_WE#	98	A_OE#	150	LATCH	202	PAR
47	B_A21	99	A_IORD#	151	CLOCK	203	C/BE1#
48	B_A16	100	A_A11	152	DATA	204	AD15
49	B_A22	101	A_IOWR#	153	GND	205	AD14
50	B_A15	102	A_A9	154	IRQ3/INTA#	206	AD13
51	B_A23	103	A_A17	155	IRQ4/INTB#	207	GND
52	B_A12	104	A_A8	156	IRQ5	208	AD12

Table B-3 16-bit PC Card Signal Names Sorted by Terminal Number

Signal Name	No.	Signal Name	No.	Signal Name	No.	Signal Name	No.
A_A0	133	A_READY(IREQ#)	135	B_A12	52	CLOCK	151
A_A1	132	A_REG#	130	B_A13	41	DATA	152
A_A2	131	A_RESET	124	B_A14	43	DEVSEL#	197
A_A3	128	A_VS1#	134	B_A15	50	FRAME#	193
A_A4	126	A_VS2#	122	B_A16	48	GND	13
A_A5	125	A_WAIT#	136	B_A17	37	GND	22
A_A6	123	A_WE#	110	B_A18	40	GND	44
A_A7	119	A_WP(IOIS16#)	139	B_A19	42	GND	75
A_A8	104	AD0	15	B_A20	45	GND	96
A_A9	102	AD1	14	B_A21	47	GND	129
A_A10	95	AD2	12	B_A22	49	GND	153
A_A11	100	AD3	11	B_A23	51	GND	167
A_A12	117	AD4	10	B_A24	53	GND	181
A_A13	106	AD5	9	B_A25	55	GND	194
A_A14	108	AD6	8	B_BVD1(STSCHG/RI#)	72	GND	207
A_A15	115	AD7	6	B_BVD2(SPKR#)	71	GNT#	168
A_A16	112	AD8	4	B_CD1#	16	IDSEL	182
A_A17	103	AD9	3	B_CD2#	74	IRDY#	195
A_A18	105	AD10	2	B_CE1#	28	IRQ3/INTA#	154
A_A19	107	AD11	1	B_CE2#	30	IRQ4/INTB#	155
A_A20	109	AD12	208	B_D0	76	IRQ5	156
A_A21	111	AD13	206	B_D1	78	IRQ7/PCDMAREQ#	157
A_A22	114	AD14	205	B_D2	80	IRQ9/IRQSER	158
A_A23	116	AD15	204	B_D3	17	IRQ10	159
A_A24	118	AD16	191	B_D4	19	IRQ11/PCDMAGNT#	160
A_A25	121	AD17	190	B_D5	21	IRQ12	161
A_BVD1(STSCHG/RI#)	138	AD18	189	B_D6	24	IRQ14	162
A_BVD2(SPKR#)	137	AD19	188	B_D7	26	IRQ15/RI_OUT	163
A_CD1#	82	AD20	186	B_D8	77	LATCH	150
A_CD2#	140	AD21	185	B_D9	79	PAR	202
A_CE1#	94	AD22	184	B_D10	81	PCLK	165
A_CE2#	97	AD23	183	B_D11	18	PERR#	199
A_D0	141	AD24	179	B_D12	20	REQ#	169
A_D1	144	AD25	178	B_D13	23	RSTIN#	166
A_D2	146	AD26	177	B_D14	25	SERR#	200
A_D3	83	AD27	176	B_D15	27	SPKROUT#/SUSPEND#	149
A_D4	85	AD28	174	B_INPACK	61	STOP#	198
A_D5	88	AD29	173	B_IORD#	33	TRDY#	196
A_D6	90	AD30	171	B_IOWR#	35	V <sub>CC</sub>	7
A_D7	92	AD31	170	B_OE#	32	V <sub>CC</sub>	31
A_D8	142	B_A0	67	B_READY(IREQ#)	69	V <sub>CC</sub>	64
A_D9	145	B_A1	66	B_REG#	63	V <sub>CC</sub>	86
A_D10	147	B_A2	65	B_RESET	58	V <sub>CC</sub>	113
A_D11	84	B_A3	62	B_VS1#	68	V <sub>CC</sub>	143
A_D12	87	B_A4	60	B_VS2#	56	V <sub>CC</sub>	164
A_D13	89	B_A5	59	B_WAIT#	70	V <sub>CC</sub>	175
A_D14	91	B_A6	57	B_WE#	46	V <sub>CC</sub>	187
A_D15	93	B_A7	54	B_WP(IOIS16#)	73	V <sub>CC</sub>	201
A_INPACK	127	B_A8	39	C/BE0#	5	V <sub>CCA</sub>	120
A_IORD#	99	B_A9	36	C/BE1#	203	V <sub>CCB</sub>	38
A_IOWR#	101	B_A10	29	C/BE2#	192	V <sub>CCP</sub>	148
A_OE#	98	B_A11	34	C/BE3#	180	V <sub>CCP</sub>	172

Table B-4 16-bit PC Card Signal Names Sorted Alphabetically



# Appendix C

## 68 Pin PC Card Socket

Zoomed Video	16-Bit	CardBus			CardBus	16-Bit	Zoomed Video
GND	GND	GND	35	1	GND	GND	GND
CD#1	CD1#	CCD1#	36	2	CAD0	D3	D3
D11	D11	CAD2	37	3	CAD14	D4	D4
D12	D12	CAD4	38	4	CAD3	D5	D5
D13	D13	CAD6	39	5	CAD5	D6	D6
D14	D14	RFU	40	6	CAD7	D7	D7
D15	D15	CAD8	41	7	CC/BE0#	CE1#	CE#1
CE#2	CE2#	CAD10	42	8	CAD9	A10	HREF
VS1#	VS1#	CVS1	43	9	CAD11	OE#	OE#
RSVD	RSVD	CAD13	44	10	CAD12	A11	VSYNC
RSVD	RSVD	CAD15	45	11	CAD14	A9	Y0
Y1	A17	CAD16	46	12	CC/BE1#	A8	Y2
Y3	A18	RFU	47	13	CPAR	A13	Y4
Y5	A19	CBLOCK#	48	14	CPERR#	A14	Y6
Y7	A20	CSTOP#	49	15	CGNT#	WE#	WE#
UV0	A21	CDEVSEL#	50	16	CINT#	READY	READY
Vcc	Vcc	Vcc	51	17	Vcc	Vcc	Vcc
Vpp2	Vpp2	Vpp2	52	18	Vpp1	Vpp1	Vpp1
UV1	A22	CTRDY#	53	19	CCLK	A16	UV2
UV3	A23	CFRAME#	54	20	CIRDY#	A15	UV4
UV5	A24	CAD17	55	21	CC/BE2#	A12	UV6
UV7	A25	CAD19	56	22	CAD18	A7	SCLK
VS2#	VS2#	CVS2	57	23	CAD20	A6	MCLK
RESET	RESET	CRST	58	24	CAD21	A5	RSVD
WAIT#	WAIT#	CSERR#	59	25	CAD22	A4	RSVD
LRCLK	RSVD	CREQ#	60	26	CAD23	A3	A[3::0]
REG#	REG#	CC/BE3#	61	27	CAD24	A2	A[3::0]
SDATA	BVD2	CAUDIO#	62	28	CAD25	A1	A[3::0]
BVD1	BVD1	CSTSCHG	63	29	CAD26	A0	A[3::0]
D8	D8	CAD28	64	30	CAD27	D0	D0
D9	D9	CAD30	65	31	CAD29	D1	D1
D10	D10	CAD31	66	32	RFU	D2	D2
CD2#	CD2#	CCD2#	67	33	CCLKRUN#	WP	PCLK
GND	GND	GND	68	34	GND	GND	GND

Figure B-2 PC Card Socket Pinouts Host Socket's View