SYCARD
TECHNOLOGY

PCCextend 145
HP Logic Analyzer Adapter
User's Manual

Preliminary

M200014-00
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1.0 Introduction

Sycard Technology's PCCextend 145 HP Logic Analyzer Adapter is an accessory product for the PCCextend 140 CardBus extender card. The PCCextend 145 is designed to interface with HP logic analyzers that use the 015650-63203 termination adapter.

- For use with Sycard's PCCextend 140 CardBus extender Card
- Supports all HP logic analyzers that use the HP 01650-63203 Termination Adapter
- Multi-layer construction to insure a low noise environment
- Connects to all CardBus signals
- Supports both timing and state analysis
- Unused logic analyzer inputs accessible via test points.
- Plug-in design allows for easy attachment and removal

2.0 Using the PCCextend 145

The PCCextend 145 is installed onto the two 34 pin headers located on the PCCextend 140 CardBus extender. The adapter should be mounted in the orientation shown in figure 2.0-1. Once installed, the HP termination adapters (on pods 1-4) can be installed on connectors JP1 through JP4.

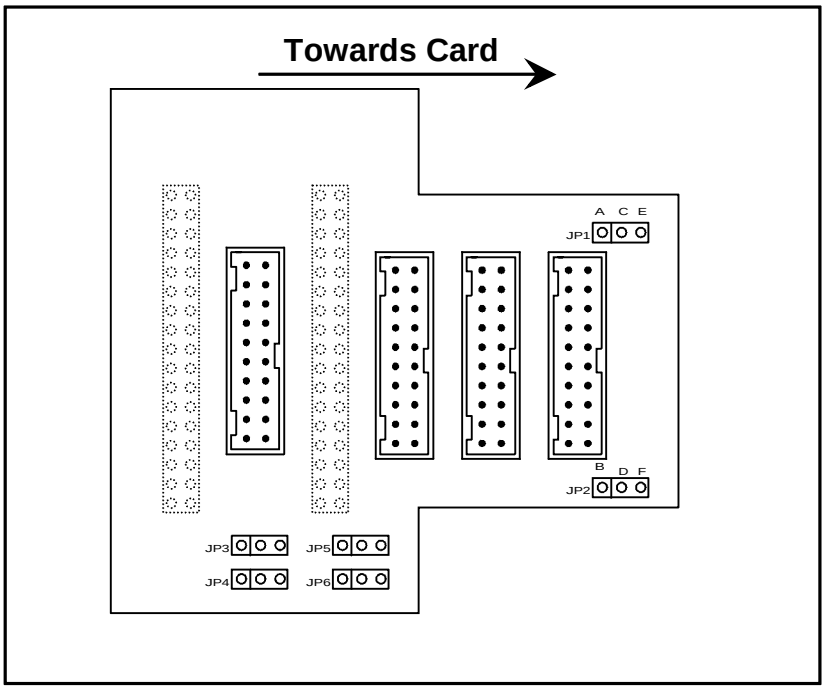


Figure 2.0-1 The PCCextend 140

The following tables list the mapping of signals to the HP Logic analyzer adapter:

Analyzer Input	CardBus Signal	CardBus Signal Pin	Note
Pod 1-0	CCBE0#	7	
Pod 1-1	CCBE1#	12	
Pod 1-2	CCBE2#	21	
Pod 1-3	CCBE3#	61	
Pod 1-4	CREQ#	60	
Pod 1-5	CGNT#	15	
Pod 1-6	CFRAME#	54	Tie JP5-2 to JP5-3
Pod 1-7	CDEVSL#	50	
Pod 1-8	CIRDY#	20	Tie JP6-2 to JP6-3
Pod 1-9	CTRDY#	53	Tie JP3-2 to JP3-3
Pod 1-10	CPAR	13	
Pod 1-11	CSTOP#	49	
Pod 1-12	CBLOCK#	48	
Pod 1-13	CPERR#	14	
Pod 1-14	CSERR#	59	
Pod 1-15	CCLK	19	Tie JP4-2 to JP4-3
Pod 2-0	CAD0	2	
Pod 2-1	CAD1	3	
Pod 2-2	CAD2	37	
Pod 2-3	CAD3	4	
Pod 2-4	CAD4	38	
Pod 2-5	CAD5	5	
Pod 2-6	CAD6	39	
Pod 2-7	CAD7	6	
Pod 2-8	CAD8	41	
Pod 2-9	CAD9	8	
Pod 2-10	CAD10	42	
Pod 2-11	CAD11	9	
Pod 2-12	CAD12	10	
Pod 2-13	CAD13	44	
Pod 2-14	CAD14	11	
Pod 2-15	CAD15	45	

Table 2.1-1A Signal Connection Table - Part A

Analyzer Input	CardBus Signal	CardBus Signal Pin	Note
Pod 3-0	CAD16	46	
Pod 3-1	CAD17	55	
Pod 3-2	CAD18	22	
Pod 3-3	CAD19	56	
Pod 3-4	CAD20	23	
Pod 3-5	CAD21	24	
Pod 3-6	CAD22	25	
Pod 3-7	CAD23	26	
Pod 3-8	CAD24	27	
Pod 3-9	CAD25	28	
Pod 3-10	CAD26	29	
Pod 3-11	CAD27	30	
Pod 3-12	CAD28	64	
Pod 3-13	CAD29	31	
Pod 3-14	CAD30	65	
Pod 3-15	CAD31	66	
Pod 4-0	CCLKRN#	33	
Pod 4-1	CINT#	16	
Pod 4-2	CAUDIO	62	
Pod 4-3	CRST#	58	
Pod 4-4	RFU1	32	
Pod 4-5	RFU2	40	
Pod 4-6	RFU3	47	
Pod 4-7	CSTSCHG	63	
Pod 4-8	CVS1	43	
Pod 4-9	CVS2	57	
Pod 4-10	JP1-1 (A)	-	General Purpose Input
Pod 4-11	JP2-3 (F)	-	General Purpose Input
Pod 4-12	JP1-2 (C)	-	General Purpose Input
Pod 4-13	JP2-2 (D)	-	General Purpose Input
Pod 4-14	JP1-3 (E)	-	General Purpose Input
Pod 4-15	JP2-3 (F)	-	General Purpose Input

Table 2.1-1B Signal Connection Table - Part A

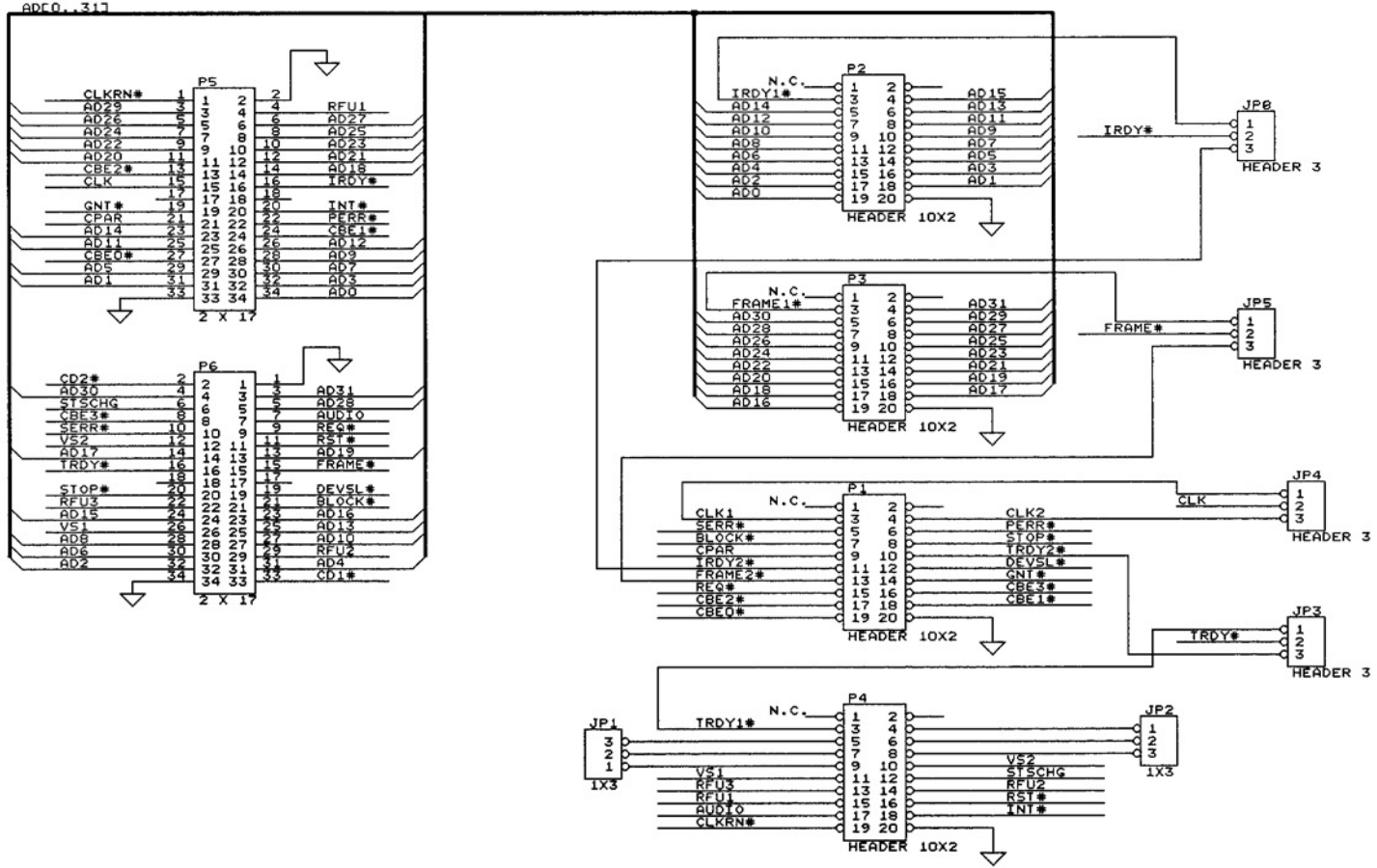
The PCCextend 145 was designed to be used as state or timing analyzer. Jumpers JP3 through JP4 route selected signals to either regular input channels or CLK inputs.

JP3	CTRDY# Jumper	
	1 - 2	CTRDY# is CLK1 on POD 4
	2 - 3	CTRDY# in D9 input on POD 1
JP4	CCLK Jumper	
	1 - 2	CCLK is CLK1 on POD 1
	2 - 3	CCLK is D15 input on POD 1
JP5	CFRAME# Jumper	
	1 - 2	CFRAME# is CLK1 on POD 3
	2 - 3	CFRAME# is D6 input on POD 1
JP6	CIRDY# Jumper	
	1 - 2	CIRDY# is CLK1 on POD 2
	2 - 2	CIRDY# is D8 input on POD 1

Table 2.1-2 Timing/State Clock Select Jumpers

Appendix

A. PCCextend 145 Schematic



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